AMENDMENTS TO THE CLAIMS

Please amend the claims of the present application as set forth below. In accordance with the PTO's revised amendment format, a detailed listing of all claims has been provided. This listing of claims will replace all prior versions, and listing, of claims in the application.

By way of overview, claims 1-5, 9-27, and 31-61 are currently pending (claims 6-8 and 28-30 having been cancelled in this Response). Of the pending claims: a) Claims 1, 4, 23, 44, 52, and 57 were previously amended; b) Claims 46-51, 53-56, and 58-61 were previously added; and c) Claims 2, 3, 5, 9-22, 24-27, 31-43, and 45 are in original form.

Listing of Claims

1. (Previously Amended) A video output system for producing video signals within a video graphics workstation, the video output system comprising:

a receiver for receiving a video signal forwarded from a video signal source within the video graphics workstation;

a video pipeline for post-processing the received video signal, the video pipeline producing a post-processed video signal; and

a video output module for converting the post-processed video signal, the video output module producing a formatted video signal,

wherein the video output system is selectively coupled to a storage medium as one video signal source,

wherein the video output system is selectively coupled to a video graphics processor as another video signal source, and

wherein the video output system is selectively coupled to a video input system as another video signal source.

2. (Original) The video output system according to claim 1 wherein the video output module further comprises:

an ancillary data injector, the injector inserting ancillary data into the post-processed video signal.

3. (Original) The video output system according to claim 1, further comprising:

a generator locking device.

4. (Previously Amended) The video output system according to claim 1 wherein the video output module includes a generator locking device.

5. (Original) The video output system according to claim 1 wherein the received video signal is e-VS, wherein e-VS is an RGB encoded video signal, an RGBA encoded video signal, a YUV-Type encoded video signal, or a YUVA-Type encoded video signal.

6. (Cancelled)

7. (Cancelled)

8. (Cancelled)

9. (Original) The video output system according to claim 1 wherein the formatted video signal is VS, wherein VS is an analog composite video signal, an analog component video signal, a serial digital composite video signal, a serial digital component video signal, a parallel digital composite video signal, or a parallel digital component video signal.

10. (Original) The video output system according to claim 1 wherein the process of post-processing includes region of interest selection.

11. (Original) The video output system according to claim 1 wherein the process of post-processing includes frame rate matching.

12. (Original) The video output system according to claim 1 wherein the process of post-processing includes spatial adaptation.

13. (Original) The video output system according to claim 12 wherein the process of spatial adaptation includes scaling.

14. (Original) The video output system according to claim 12 wherein the process of spatial adaptation includes picture framing.

15. (Original) The video output system according to claim 14 wherein the process of picture framing includes letter boxing.

16. (Original) The video output system according to claim 1 wherein the process of post-processing includes changing the sample rate of the video signal being post-processed.

17. (Original) The video output system according to claim 1 wherein the process of post-processing includes gamma removal.

18. (Original) The video output system according to claim 1 wherein the process of post-processing includes gamma insertion.

19. (Original) The video output system according to claim 1 wherein the process of post-processing includes color space conversion.

20. (Original) The video output system according to claim 1 wherein the process of post-processing includes changing frames of video data into interleaved fields of video data.

21. (Original) The video output system according to claim 1 wherein the process of post-processing includes addressing on a frame-by-frame basis the video signal being post-processed.

22. (Original) The video output system according to claim 1 wherein the system is a Peripheral Component Interconnect circuit board.

23. (Previously Amended) A method for producing video signals using a video output system within a video graphics workstation, the method comprising:

receiving a video signal in a receiver of the video output system, wherein the video signal is forwarded from a video signal source within the video graphics workstation;

post-processing the received video signal through a video pipeline of the video output system, producing a post-processed video signal; and

converting the post-processed video signal in a video output module of the video output system, producing a formatted video signal,

wherein the video output system is selectively coupled to a storage medium as one video signal source,

wherein the video output system is selectively coupled to a video graphics processor as another video signal source, and

wherein the video output system is selectively coupled to a video input system as another video signal source.

24. (Original) The method according to claim 23, further comprising:

inserting ancillary data into the post-processed video signal prior to converting the post-processed video signal.

25. (Original) The method according to claim 23, further comprising:

generator locking the received video signal.

26. (Original) The method according to claim 23 wherein the video output module includes a generator locking device.

27. (Original) The method according to claim 23 wherein the received video signal is e-VS, wherein e-VS is an RGB encoded video signal, an RGBA encoded video signal, a YUV-Type encoded video signal, or a YUVA-Type encoded video signal.

28. (Cancelled)

29. (Cancelled)

30. (Cancelled)

31. (Original) The method according to claim 23 wherein the formatted video signal is VS, wherein VS is an analog composite video signal, an analog component video signal, a serial digital composite video signal, a serial digital component video signal, a parallel digital composite video signal, or a parallel digital component video signal.

32. (Original) The method according to claim 23 wherein the process of post-processing includes region of interest selection.

33. (Original) The method according to claim 23 wherein the process of post-processing includes frame rate matching.

34. (Original) The method according to claim 23 wherein the process of post-processing includes spatial adaptation.

35. (Original) The method according to claim 34 wherein the process of spatial adaptation includes scaling.

36. (Original) The method according to claim 34 wherein the process of spatial adaptation includes picture framing.

37. (Original) The method according to claim 36 wherein the process of picture framing includes letter boxing.

38. (Original) The method according to claim 23 wherein the process of post-processing includes changing the sample rate of the video signal being post-processed.

39. (Original) The method according to claim 23 wherein the process of post-processing includes gamma removal.

40. (Original) The method according to claim 23 wherein the process of post-processing includes gamma insertion.

41. (Original) The method according to claim 23 wherein the process of post-processing includes color space conversion.

42. (Original) The method according to claim 23 wherein the process of post-processing includes changing frames of video data into interleaved fields of video data.

43. (Original) The method according to claim 23 wherein the process of post-processing includes addressing on a frame-by-frame basis the video signal being post-processed.

44. (Previously Amended) A video output system for producing video signals within a video graphics workstation, the video output system comprising:

means for receiving a video signal forwarded from a video signal source within the video graphics workstation;

means for post-processing the received video signal through a video pipeline, producing a post-processed video signal; and

means for converting the post-processed video signal, producing a formatted video signal,

wherein the video output system is selectively coupled to a storage medium as one video signal source,

wherein the video output system is selectively coupled to a video graphics processor as another video signal source, and

wherein the video output system is selectively coupled to a video input system as another video signal source.

45. (Original) The system according to claim 44, further comprising:

means for inserting ancillary data into the post-processed video signal prior to converting the post-processed video signal.

46. (Previously added) The video output system according to claim 1 wherein the receiver and the video pipeline are implemented as an integrated video processing module, and wherein the video output module is detachably coupled to the video processing module.

47. (Previously Added) The video output system according to claim 46 wherein the video output module is a daughterboard module that couples to the video processing module.

48. (Previously Added) The video output system according to claim 46 wherein the video output module includes a processor that is configured to inform the video processing module of its configuration.

49. (Previously Added) The method according to claim 23 wherein the receiver and the video pipeline are implemented as an integrated video processing module, and wherein the method further includes detachably coupling the video output module to the video processing module.

50. (Previously Added) The method according to claim 49 wherein the video output module is a daughterboard module that couples to the video processing module.

51. (Previously Added) The method according to claim 49 wherein the video output module includes a processor, and wherein the processor informs the video processing module of its configuration.

52. (Previously Amended) A video output system for producing video signals, the video output system being selectively coupled to a video graphics processor, a video signal input system, and a storage medium for storing data in electrical form, the video output system comprising:

a receiver for receiving a video signal;

a video pipeline for post-processing the received video signal, the video pipeline producing a post-processed video signal; and

a video output module for converting the post-processed video signal, the video output module producing a formatted video signal,

wherein the received video signal is selectively forwarded from:

the storage medium, when the receiver of the video output system is coupled to the storage medium;

the video graphics processor, when the receiver of the video output system is coupled to the storage medium; ~~or~~ and

the video signal input system, when the receiver of the video output system is coupled to video signal input system.

53. (Previously Added) The video output system according to claim 52 wherein the video pipeline is configured to perform plural functions selected from the following functions:

region of interest selection;

frame rate matching;

spatial adaptation;

changing the sample rate of the video signal being post-processed;

gamma removal;

gamma insertion;

color space conversion;

changing frames of video data into interleaved fields of video data; and

addressing on a frame-by-frame basis the video signal being post-processed.

54. (Previously Added) The video output system according to claim 53 wherein the video pipeline includes functionality for performing each said function.

55. (Previously Added) The video output system according to claim 53 wherein the video output module further comprises at least one of:

an ancillary data injector, the injector inserting ancillary data into the post-processed video signal; and

a generator locking device.

56. (Previously Added) The video output system according to claim 54 wherein the video output module further comprises:

an ancillary data injector, the injector inserting ancillary data into the post-processed video signal; and

a generator locking device.

57. (Previously Amended) A method for producing video signals using a video output system, the video output system being selectively coupled to a video graphics processor, a video signal input system, and a storage medium for storing data in electrical form, the method comprising:

receiving a video signal in a receiver of the video output system;

post-processing the received video signal through a video pipeline of the video output system, producing a post-processed video signal; and

converting the post-processed video signal in a video output module of the video output system, producing a formatted video signal,

wherein the received video signal is selectively forwarded from:

the storage medium, when the receiver of the video output system is coupled to the storage medium;

the video graphics processor, when the receiver of the video output system is coupled to the storage medium; ~~or~~ and

the video signal input system, when the receiver of the video output system is coupled to video signal input system.

58. (Previously Added) The method according to claim 57 wherein the video pipeline performs plural functions selected from the following functions:

region of interest selection;

frame rate matching;

spatial adaptation;

changing the sample rate of the video signal being post-processed;

gamma removal;

gamma insertion;

color space conversion;

changing frames of video data into interleaved fields of video data; and

addressing on a frame-by-frame basis the video signal being post-processed.

59. (Previously Added) The method according to claim 58 wherein the video pipeline includes functionality for performing each said function.

60. (Previously Added) The method according to claim 58, further comprising performing at least one of:

inserting ancillary data into the post-processed video signal; and

generator locking the receive video signal.

61. (Previously Added) The method according to claim 59, further comprising:

inserting ancillary data into the post-processed video signal; and

generator locking the receive video signal.